



300-420^{Q&As}

Designing Cisco Enterprise Networks (ENSLD)

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QUESTION 1

An architect must develop a campus network solution that includes:

1.
logically segmented and isolated networks
2.
ability to communicate between network segments when required
3.
support for overlapping IP addresses
4.
widely available technologies to avoid purchasing specialized equipment

Which solution must the architect select?

- A. VSS with IGP
- B. 802.1Q with HSRP
- C. vPC with HSRP
- D. VRF-Lite with OSPF

Correct Answer: D

QUESTION 2

Which design element should an engineer consider when multicast is included in a Cisco SD-Access architecture?

- A. PIM SSM must run in the underlay.
- B. Multicast clients reside in the underlay, and the multicast source is outside the fabric or in the overlay.
- C. Rendezvous points must be used in a PIM SSM deployment.
- D. Multicast traffic is transported in the overlay and the EID space for wired and wireless clients.

Correct Answer: D

Multicast traffic is transported in the overlay, in the EID space, for both wired and wireless clients

<https://www.ciscolive.com/c/dam/r/ciscolive/us/docs/2018/pdf/BRKEWN-2020.pdf> <https://www.cisco.com/c/dam/en/us/td/docs/cloud-systems-management/network-automation-and-management/dna-center/deploy-guide/cisco-dna-center-sd-access-wl-dg.pdf>





QUESTION 4

Which two best practices must be followed when designing an out-of-band management network? (Choose two.)

- A. Enforce access control
- B. Facilitate network integration
- C. Back up data using the management network
- D. Ensure that the management network is a backup to the data network
- E. Ensure network isolation

Correct Answer: AE

https://www.cisco.com/c/en/us/td/docs/solutions/Enterprise/Security/SAFE_RG/SAFE_rg/c hap9.html

QUESTION 5

An engineer must design a large Layer 2 domain that contains hundreds of switches and VLANs. The engineer's primary goals are to:

1.
Efficiently utilize the bandwidth of all links
2.
Avoid Layer 2 loops
3.
Cause minimal impact on switch CPU and memory Which technology should the engineer include in the design?

- A. MST
- B. Rapid PVST+
- C. RSTP
- D. PVST+

Correct Answer: A