



C4070-604^{Q&As}

IBM System z Technical V5

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QUESTION 1

Which new z196 feature provides significant error detection and correction capabilities for bit, lane, DRAM, DIMM socket and memory channel failures?

- A. RAS
- B. RAID
- C. RACEv
- D. RAIM

Correct Answer: D

QUESTION 2

A new customer bought a zEnterprise 114 and zBX. The customer questioned why there are two HMCs configured. Which of the following is the reason that the HMC needs to be redundant?

- A. zEnterprise 114 and zBX will have a dedicated HMC respectively
- B. The Support Element (SE) is no longer available
- C. All zEnterprise servers are configured with 2 sets of HMC by default
- D. Two HMCs are required to preserve Ensemble state configuration

Correct Answer: D

QUESTION 3

A customer recently ordered a z196 with a zBX.

Which of the following is the best action a specialist could take to ensure the customer is satisfied with the new zBX?

- A. Review the hardware support process and the HMC documentation for the z196 CPC and the zBX
- B. Conduct and "Fit for Purpose Study"
- C. Conduct a z/VM Implementation Workshop
- D. Mentor the customer on installing PowerVM and VMware

Correct Answer: A



QUESTION 4

Which of the following best describes zEnterprise architectural advantages?

- A. A System of Systems consisting of multiple architectures
- B. A centralized shared everything z/Architecture
- C. Shared nothing architecture
- D. Includes all IBM servers and storage systems

Correct Answer: A

QUESTION 5

z196 has better performance than z10 EC. Which of the following is the reason?

- A. RAIM memory
- B. Additional Cryptographic algorithm
- C. More I/O bandwidth
- D. Faster processor speed

Correct Answer: D

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