EN0-001 Q&As

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QUESTION 1

An advantage of removable flash memory over built-in flash memory is that:

A. Storage can be easily replaced, for example to increase capacity.

B. It is quicker to access, providing far greater bandwidth for read operations.

C. It has a longer life, indicated by being rated for a higher number of write cycles.

D. It takes up less physical space in a device, and does not require any space on the printed circuit board.

Correct Answer: A

QUESTION 2

Which power mode describes the state where the ARM processor is powered down, but its Level 1 caches remain powered?

A. Run mode

B. Dormant mode

C. Standby mode

D. Shutdown mode

Correct Answer: B

QUESTION 3

In which of the following scenarios would cache maintenance operations be necessary in an ARMv7 system?

A. Before executing code that uses the NEON instruction set

B. Before handling an interrupt request raised by an external device

C. Before checking the status of a semaphore

D. Before reading cacheable memory that has been written to by an external bus master

Correct Answer: D

QUESTION 4

In an ARMv7-A system, the following C function calculates a simple checksum for an input data packet of variable length. The checksum is defined to be the sum of all of the 16-bit data items in the packet modulo 65536. The parameter data_items contains the number of 2-byte data items in the packet, and it cannot be zero by design.

```
unsigned short checksum(unsigned short * data, unsigned short data_items)
{
    unsigned short i;
    unsigned int sum = 0;

    for (i = 0; i < data_items; i++)
    {
        sum += data[i];
    }

    return (unsigned short)(sum % 65536);
}
```

When using an ARM compiler, which TWO of the following optimizations could improve the performance of this code? (Choose two)

A. Use a do/while loop instead of a for loop

B. Change the type of sum to be an unsigned short

C. Change the type of i to be an unsigned int

D. Use signed variables instead of unsigned variables

E. Declare sum as a global variable

Correct Answer: AC

QUESTION 5

Which one of the following statements best describes the function of vector catch logic?

A. It traps writes to the memory containing the vector table

B. It provides additional resources for debugging exception handlers

C. It provides configurable exception priorities on an ARM processor

D. It provides an improved mechanism for an application to handle exceptions

Correct Answer: B