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QUESTION 1

In an ARMv7-R processor, with which level of the memory system is the Memory Protection Unit (MPU) associated?

- A. Level 1
- B. Level 2
- C. Level 3
- D. Level 4

Correct Answer: A

QUESTION 2

As part of the ABI specification, the AAPCS defines which of the following?

- A. How many levels of nested function calls are permitted on ARM systems
- B. How to measure the maximum amount of stack required by an application
- C. On which mode's stack you need to save the return address in a non-leaf function
- D. Which registers need to be preserved by a function

Correct Answer: D

QUESTION 3

In which type of storage will the compiler preferentially place frequently accessed variables?

- A. Stack
- B. Heap
- C. Registers
- D. Hard disk

Correct Answer: C

QUESTION 4

Which of the following features was added in version 2 of the ARM Architecture Advanced SIMD extensions?

- A. Additional quadword registers
- B. Support for double precision floating-point arithmetic



C. Fused Multiply-Accumulate (Fused MAC) instructions

D. Support for polynomials

Correct Answer: C

QUESTION 5

If the performance of an application remains unchanged when the core clock speed of a Cortex-A9 processor is reduced, what can you deduce about the system?

A. The Clocks Per Instruction (CPI) of the processor has increased

B. The processor is NOT the limiting factor on performance

C. Instruction cache utilization has improved

D. The core has stopped carrying out speculative data memory accesses

Correct Answer: B

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