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QUESTION 1

What is the maximum value of the immediate field in an ARM SVC instruction?

- A. 0x0
- B. 0xF
- C. 0xFF
- D. 0xFFFFFFFF

Correct Answer: D

QUESTION 2

Which of these C99 keywords can be used to indicate that two arrays do not overlap?

- A. "pure"
- B. "volatile"
- C. "static"
- D. "restrict"

Correct Answer: D

QUESTION 3

Which privileged mode can kernel code use to get direct access to the User mode registers R13 and R14?

- A. Abort mode
- B. System mode
- C. Hypervisor mode
- D. Supervisor mode

Correct Answer: B

QUESTION 4

Which of the following pairs of statements about the difference between a Memory Management Unit (MMU) and a Memory Protection Unit (MPU) is correct?

- A. The MMU uses translation tables. The MPU does not use translation tables.



- B. The MMU uses only physical addresses. The MPU translates virtual addresses to physical addresses.
- C. The MMU defines cacheability attributes for memory. The MPU does not define cacheability attributes for memory.
- D. The MMU defines access permissions for memory. The MPU does not define access permissions for regions of memory.

Correct Answer: A

QUESTION 5

Under which of the following data-sharing scenarios would cache maintenance operations be necessary?

- A. Sharing data with another thread running on the same core
- B. Sharing data with another process running on the same core
- C. Sharing data with an external device
- D. Sharing data with another CPU in an SMP system

Correct Answer: C

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