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QUESTION 1

The Memory Protection Unit (MPU) of Cortex-R4 performs which of the following tasks?

A. Translates virtual addresses to physical addresses

B. Generates parity information to detect soft errors in memory

C. Performs access permission checks

D. Permits the system to be divided into secure and normal worlds, through the use of ARM\\'s TrustZone technology

Correct Answer: C

QUESTION 2

In the Generic Interrupt Controller (GIC) architecture, which of the following ID numbers are reserved for interrupts that are private to a CPU interface?

A. ID0-ID7

B. ID0-ID15

C. ID0-ID31

D. ID0-ID63

Correct Answer: C

QUESTION 3

An application contains three calls to an external function, foobar(), which is defined in a shared (or dynamic) library. How many copies of foobar() will the linker place in the application image?

(Ignore linker inlining)

A. None

B. Always one

C. Always three

D. One or more depending on optimization level

Correct Answer: A

QUESTION 4

Which power mode describes the state where the ARM processor is powered down, but its Level 1 caches remain

powered?

A. Run mode

B. Dormant mode

C. Standby mode

D. Shutdown mode

Correct Answer: B

QUESTION 5

What view in a debugger displays the order in which functions were called?

A. The Call Stack view

B. The Memory view

C. The Registers view

D. The Variables view

Correct Answer: A

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