

### HP2-T16<sup>Q&As</sup>

**Industry Standard Architecture and Technology** 

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### **QUESTION 1**

You are receiving performance	complains about	a client/server-based application.	Given the following information:

Network Output Queue Length: 10
% Processor Time: 50%
% Disk time: 30%
Pages/Sec: 3
Which server subsystem is the bottleneck?
A. processor
B. network connection
C. memory
D. disk
Correct Answer: B
{\rtf1\ansi\ansicpg936\deff0\deflang1033\deflangfe2052{\fonttbl{\fo\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\fo\fs17 Industry Standard Architecture - Student Guide 2 - Page 246:\par Network interface counters\par Output Queue LengthReports the length of the output packet queue (in packets). If the length is 2 or longer, delays are being experienced and the bottleneck should be found and eliminated, if possible. Because the requests are queued by the Network Driver Interface Specification (NDIS) in this implementation, this counter should always be zero.\par }
QUESTION 2
Which RAID technology provides the most capacity with the same number of disks?
A. RAID 0
B. RAID 1
C. RAID 4
D. RAID 5
Correct Answer: A

### **QUESTION 3**

In systems with AMD processors, what allows communication between processors and the I/O subsystem?

A. Northbridge

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- B. Southbridge
- C. HyperTransport link
- D. APIC
- E. QuickPath Interconnect

Correct Answer: C

{\rtf1\ansi\ansicpg936\deff0\deffang1033\deflangfe2052{\fonttbl{\fo\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\fo\fs17 Industry Standard Architecture - Student Guide 1 - Page 37\par AMD processors are able to communicate with each other through HyperTransport point-to-point links. This enables one processor to access the memory connected to another processor. Inside the processor, a crossbar switch connects the processor, memory controller, and HyperTransport links.\par AMD processors also use HyperTransport links to connect to the I/O subsystem.\par The links on particular processors are connected to I/O tunnels that support the I/O devices.\par All other processors can communicate with the I/O system through the HyperTransport links.\par Legacy devices are also connected to one of the I/O tunnels.\par }

#### **QUESTION 4**

Which protocols are support in FC-SAN? (Select three)

- A. Arbitrated Loop
- B. Ethernet
- C. Switched Fabric
- D. Point to Point
- E. CSMA/CD
- F. Token Ring

Correct Answer: ACD

{\rtf1\ansi\ansicpg936\deff0\deffang1033\deflangfe2052{\fonttbl{\fo\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\fo\fs17 Industry Standard Architecture - Student Guide 2 - Page 15\par Fibre Channel technology greatly enhances flexibility by providing the following advantages:\par Multiple topologies (point to point, arbitrated loop, and switched fabric)\par }

#### **QUESTION 5**

What is the most commonly used measurement unit for describing a UPS?

- A. kW
- B. VA
- C. Amps
- D. kJ



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Correct Answer: B

{\rtf1\ansi\ansicpg936\deff0\deflang1033\deflangfe2052{\fonttbl{\f0\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 187\par An uninterruptible power supply (UPS) system provides power to the server in case of loss of electrical power from the main building power. The UPS is rated in volt-amps (VA) which is the total power it can handle and the time it can run the server, usually the time required for the operating system to close all running applications, gracefully shut itself down, and turn off the server.\par }

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