C1000-031^{Q&As}

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QUESTION 1

A customer with critical up-time requirements is comparing POWER9 processor-based servers.

Which RAS feature is found only on the Power System E980?

A. Redundant Service Processors

B. Hot swap fans

C. Chipkill memory

D. Split backplane

Correct Answer: A

Reference: http://www-01.ibm.com/common/ssi/ShowDoc.wss?docURL=/common/ssi/rep_sm/3/877/
ENUS9080-_h03/index.htmlandrequest_locale=en

QUESTION 2

A customer\'s infrastructure is currently based on a Power System E870 managed by an HMC 7042-CR6 at V8.860 SW
level. They are planning to upgrade their server to a new Power System E980.

Which of the following will enable the customer to connect an HMC to the Power System E980?

A. Install vHMC on a partition on the Power System E980.

B. Order a new Power-based HMC (7063-CR1).

C. Upgrade the software on the existing HMC.

D. Use the existing HMC at the current SW level.

Correct Answer: C

QUESTION 3

What is an advantage of running Linux on Power compared to x86 technology?

A. Extended Error Handling

B. Docker containers

C. Chipkill memory

D. Partition mobility

Correct Answer: D

Reference: https://www.ibm.com/developerworks/library/l-linux-on-power/index.html

QUESTION 4

An AIX customer has a Power System E950 connected to a V9000 in one location. The customer is planning to purchase another Power System E950 and V9000 that will be installed 25 miles (40 km) from the primary location.

Which product would enable the applications to start at the secondary location, if any of the VMs failed at the primary location?

A. PowerVM Standard Edition

B. PowerVM Enterprise Edition

C. PowerHA Standard Edition

D. PowerHA Enterprise Edition

Correct Answer: C

QUESTION 5

A customer has a significant investment in open source software for performing in-memory analytics. This workload is currently deployed on Linux and recent x86 hardware.

What advantage would a POWER9 processor-based server provide this customer?

A. Faster ChipKill correction rate

B. More cache per core

C. More cores per socket

D. Faster PCIe bandwidth

Correct Answer: C

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